DiSL: An Extensible Language for Efficient and Comprehensive Dynamic Program Analysis

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Abstract

Dynamic program analysis tools support numerous software engineering tasks, including profiling, debugging, and reverse engineering. Prevailing techniques for building dynamic analysis tools are based on low-level abstractions that make tool development tedious, error-prone, and expensive. To simplify the development of dynamic analysis tools, some researchers promoted the use of aspect-oriented programming (AOP). However, as mainstream AOP languages have not been designed to meet the requirements of dynamic analysis, the success of using AOP in this context remains limited. For example, in AspectJ, join points that are important for dynamic program analysis (e.g., the execution of bytecodes or basic blocks of code) are missing, access to reflective dynamic join point information is expensive, data passing between woven advice in local variables is not supported, and the mixing of low-level bytecode instrumentation and high-level AOP code is not foreseen. In this talk, we present DiSL [1], a new domain-specific aspect language for bytecode instrumentation. DiSL uses Java annotation syntax such that standard Java compilers can be used for compiling DiSL code. The language features an open join point model. novel constructs inspired by weave-time evaluation of conditional join points and by staged execution, and access to custom static and dynamic context information. Moreover, the DiSL weaver guarantees complete bytecode coverage. We have implemented several dynamic analysis tools in DiSL, including profilers for the inter- and intra-procedural control flow, debuggers, dynamic metrics collectors integrated in the Eclipse IDE to augment the static source views with dynamic information, and tools for workload characterization. These tools are concise and perform equally well as implementations using low-level techniques. DiSL has also been conceived as an intermediate language for future domain-specific analysis languages, as well as for AOP languages.

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Danilo Ansaloni holds a MSc. in Computer Science from the University of Modena and Reggio Emilia, Italy. He is now a PhD candidate at the University of Lugano, Switzerland, in the group of Prof. Walter Binder. His research interests include dynamic program analysis, parallel computing, and programming languages.

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Copyright is held by the author/owner(s). DSAL'12, March 27, 2012, Potsdam, Germany. ACM 978-1-4503-1128-1/12/03. a MSc., a PhD, and a venia docendi from the Vienna University of Technology, Austria. Before joining the University of Lugano, he was post-doctoral researcher at the Artificial Intelligence Laboratory, Ecole Polytechnique Fédérale de Lausanne (EPFL), Switzerland. His main research interests are in the area of dynamic program analysis, virtual execution environments, programming languages, and service-oriented computing.

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References

[1] L. Marek, A. Villazón, Y. Zheng, D. Ansaloni, W. Binder, and Z. Qi. DiSL: a domain-specific language for bytecode instrumentation. In AOSD '12: Proceedings of the 11th International Conference on Aspect-Oriented Software Development, Mar. 2012.